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Date of Deposit: March 29, 2004

Patent
Our Case No. 10519/118

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)	
Vyvoda et al.)	
Serial No.:)	Examiner: Not Yet Assigned
Not Yet Assigned)	
Filed:)	Group Art Unit: Not Yet Assigned
Herewith)	
For:)	
Multiple-Mode Memory and)	
Method for Forming Same)	

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

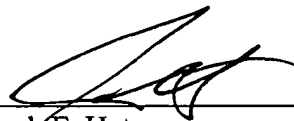
Dear Sir:

Pursuant to the obligation under 37 C.F.R. § 1.56 and in conformance with 37 C.F.R. §§ 1.97-1.99, Applicant hereby submits documents A1-A49 listed on the attached form PTO-1449 for consideration by the Examiner. In accordance with 37 C.F.R. § 1.98(d), copies of A2-A49 are not provided since they were previously cited by or submitted to the Office in prior application serial number 10/184,578, which is being relied upon for an earlier filing date under 35 U.S.C. § 120. A copy of document A1 is enclosed herewith.

The filing of this Information Disclosure Statement does not constitute an admission that the information cited herein is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b). Further, Applicant reserves the right to contest that any of these documents is prior art against the present application.

Dated: March 29, 2004

Respectfully submitted,



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FORM PTO-1449	SERIAL NO. Not Yet Assigned	CASE NO. 10519-118
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT	FILING DATE Herewith	GROUP ART UNIT Not Yet Assigned
(use several sheets if necessary)	APPLICANT(S): Vyvoda et al	

REFERENCE DESIGNATION
U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS/ SUBCLASS	FILING DATE
	A1	4,799,196	01/1989	Takemae		
	A2	5,029,125	07/1991	Sciupac		
	A3	5,249,282	09/1993	Segers		
	A4	5,285,323	02/1994	Hetherington et al.		
	A5	5,515,333	05/1996	Fujita et al.		
	A6	5,699,317	12/1997	Sartore et al.		
	A7	5,721,862	02/1998	Sartore et al.		
	A8	5,812,418	09/1998	Lattimore et al.		
	A9	5,818,748	10/1998	Bertin et al.		
	A10	5,835,396	10/1998	Zhang		
	A11	5,856,221	01/1999	Clementi et al.		
	A12	5,911,104	06/1999	Smayling et al.		
	A13	6,034,882	03/2000	Johnson et al.		
	A14	6,124,157	09/2000	Rahim		
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	A16	6,185,122	02/2001	Johnson et al.		
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	A18	6,208,545	03/2001	Leedy		
	A19	6,259,132	07/2001	Pio		
	A20	6,263,398	07/2001	Taylor et al.		
	A21	6,266,272	07/2001	Kirihata et al.		
	A22	6,378,118	04/2002	Sugibayashi		
	A23	6,424,581	07/2002	Bosch et al.		
	A24	5,889,694	03/1999	Shepard		
	A25	6,420,215	07/2002	Knall et al.		
	A26	6,545,891	04/2003	Tringali et al.		
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EXAMINER INITIAL	OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)
EXAMINER	DATE CONSIDERED

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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(use several sheets if necessary)	APPLICANT(S): Vyvoda et al	

EXAMINER INITIAL	OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)	
	A35	"MultiMediaCard System Specification Version 2.2 Official Release," pages 10, 12, and 14, January 2000.
	A36	"A 10ns Read and Write Non-Volatile Memory Array Using a Magnetic Tunnel Junction and FET Switch in each Cell," Scheuerlein et al., ISSCC 2000/Session 7/TD: Emerging Memory & Device Technologies/Paper TA 7.2, 4 pages, February 8, 2000.
	A37	"A 125mm ² /Gb NAND Flash Memory with 10MB/s Program Throughput," Nakamura et al., ISSCC 2002/Session 6/SRAM and Non-Volatile Memories/6.4, 10 pages (December 4, 2002).
	A38	"Three-Dimensional Memory Array and Method of Fabrication," Johan Knall, U.S. App. Serial No. 09/560,626 filed April 28, 2002.
	A39	"Method for Deleting Stored Digital Data from Write-Once Memory Device," Christopher S. Moore, Derek J. Bosch, Daniel C. Steere, J. James Tringali, U.S. Patent Application Serial No. 09/638,439 filed August 14, 2002.
	A40	"Memory Device with Row and Column Decoder Circuits Arranged in a Checkerboard Pattern under a Plurality of Memory Arrays," Roy E. Scheuerlein, U.S. Patent Application Serial No. 09/896,814 filed June 29, 2001
	A41	"Memory Devices and Methods for Use Therewith," Roger W. March, Christopher S. Moore, Daniel Brown, Thomas H. Lee, Mark G. Johnson, U.S. Patent Application Serial No. 09/748,589 filed December 22, 2000.
	A42	"Method for Reading Data in a Write-Once Memory Device Using a Write-Many File System," Christopher S. Moore, J. James Tringali, Roger W. March, James E. Schneider, Derek J. Bosch, Daniel C. Steere, U.S. Patent Application Serial No. 09/878,138 filed June 8, 2001.
	A43	"Method for Re-Directing Data Traffic in a Write-Once Memory," J. James Tringali, Christopher S. Moore, Roger W. March, James E. Schneider, Derek J. Bosch, Daniel C. Steere, U.S. Patent Application Serial No. 09/877,691 filed June 8, 2001.
	A44	"Memory Device and Method for Storing and Reading Data in a Write-Once Memory Array," Christopher S. Moore, James E. Schneider, J. James Tringali, Roger W. March, U.S. Patent Application Serial No. 09/877,720 filed June 8, 2001.
	A45	"Memory Device and Method for Storing and Reading a File System Structure in a Write-Once Memory Array," Christopher S. Moore, James E. Schneider, J. James Tringali, Roger W. March, U.S. Patent Application Serial No. 09/877,719 filed June 8, 2001.
	A46	"Method for Storing Digital Information in Write-Once Memory Array," David R. Friedman, Derek J. Bosch, Christopher S. Moore, Joseph J. Tringali, Michael A. Vyvoda, U.S. Patent Application Serial No. 09/727,229 filed November 30, 2000.
	A47	"Modular Memory Device," J. James Tringali, P. Michael Farmwald, Thomas H. Lee, Mark G. Johnson, Derek J. Bosch, U.S. Patent Application Serial No. 09/638,334 filed August 14, 2000.
	A48	"Low-Cost Three-Dimensional Memory Array," Mark G. Johnson, Thomas H. Lee, Vivek Subramanian, P. Michael Farmwald, N. Johan Knall, U.S. Patent Application Serial No. 09/928,969 filed August 13, 2001.
	A49	International Search Report for PCT/US03/19382, December 5, 2003 (1 page).

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